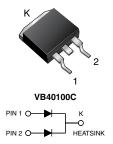
VB40100C-M3, VB40100CHM3

Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.38 \text{ V}$ at $I_F = 5 \text{ A}$





PRIMARY CHARACTERISTICS				
Package	TO-263AB			
I _{F(AV)}	2 x 20 A			
V_{RRM}	100 V			
I _{FSM}	250 A			
V_F at $I_F = 20$ A	0.61 V			
T _J max.	150 °C			
Diode variations	Common cathode			

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- · Low thermal resistance

COMPLIANT HALOGEN FREE

- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- AEC-Q101 qualified available:
 - Automotive ordering code P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: TO-263AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

ALC-Q 101 qualified

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: As marked

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER Maximum repetitive peak reverse voltage		SYMBOL	VB40100C	UNIT	
		V_{RRM}	100	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	40	А	
	per diode		20		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	250	А	
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150	°C	

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	- V _F	0.47	-	V	
	I _F = 10 A			0.54	-		
	I _F = 20 A			0.67	0.73		
	I _F = 5 A	T _A = 125 °C		0.38	-		
	I _F = 10 A			0.45	-		
	I _F = 20 A			0.61	0.67		
Reverse current at rated V _R per diode ⁽²⁾	V _R = 70 V	T _A = 25 °C	· I _R	9	-	μΑ	
	V _R = 70 V	T _A = 125 °C		10	-	mA	
	V _R = 100 V	T _A = 25 °C		-	1000	μΑ	
	v _R = 100 v	T _A = 125 °C		21	45	mA	

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VB40100C	UNIT	
Typical thermal resistance per diode	$R_{ heta JC}$	2.0	°C/W	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	VB40100C-M3/4W	1.39	4W	50/tube	Tube	
TO-263AB	VB40100C-M3/8W	1.39	8W	800/reel	Tape and reel	
TO-263AB	VB40100CHM3/I (1)	1.39	ı	800/reel	Tape and reel	

Note

(1) AEC-Q101 qualified

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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

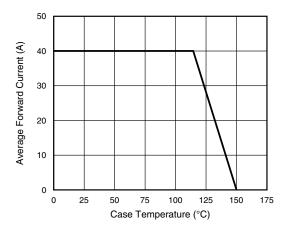


Fig. 1 - Forward Current Derating Curve

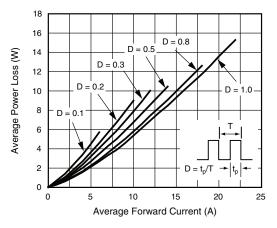


Fig. 2 - Forward Power Loss Characteristics Per Diode

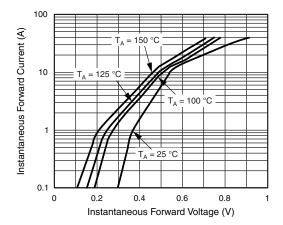


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

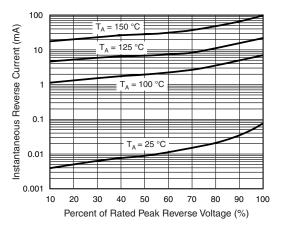


Fig. 4 - Typical Reverse Characteristics Per Diode

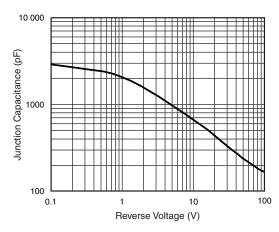


Fig. 5 - Typical Junction Capacitance Per Diode

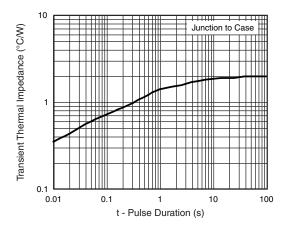


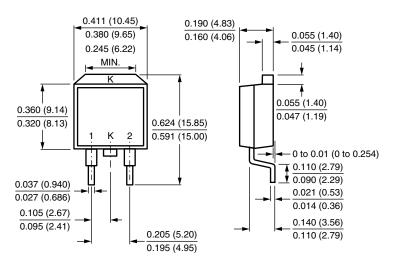
Fig. 6 - Typical Transient Thermal Impedance Per Diode



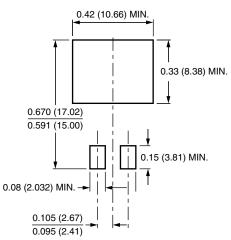
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

TO-263AB



Mounting Pad Layout





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